

Title: SELF ALIGNED TRIPLE GATE SILICON-ON-INSULATOR (SOI) DEVICE

Inventors: Krivokapic et al.

Art Unit: 2811

Appl No.: 09/711,445

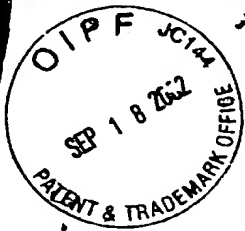
Conf. No.: 7218

Docket No.: AMDI-08399US0

Filing Date: November 13, 2000

Attorney: Stephen R. Bachmann

Telephone No.: 415-362-3800



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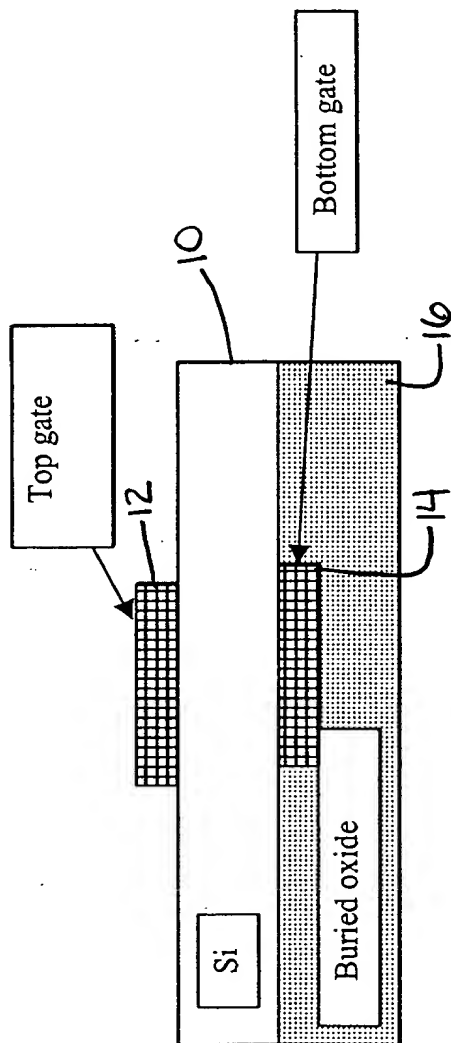


FIG. 1

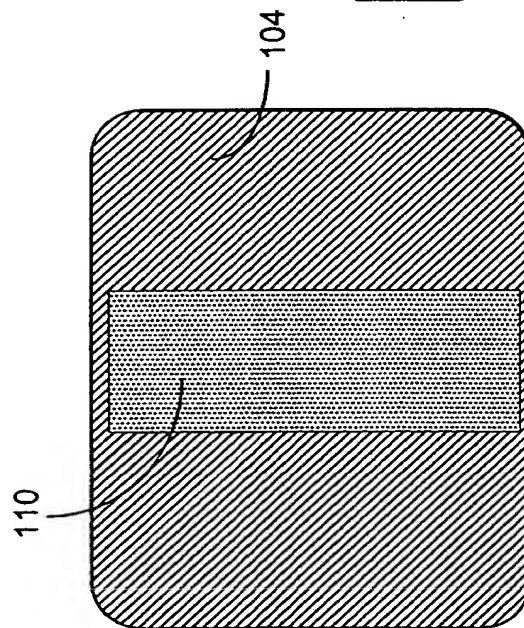


FIG. 4A

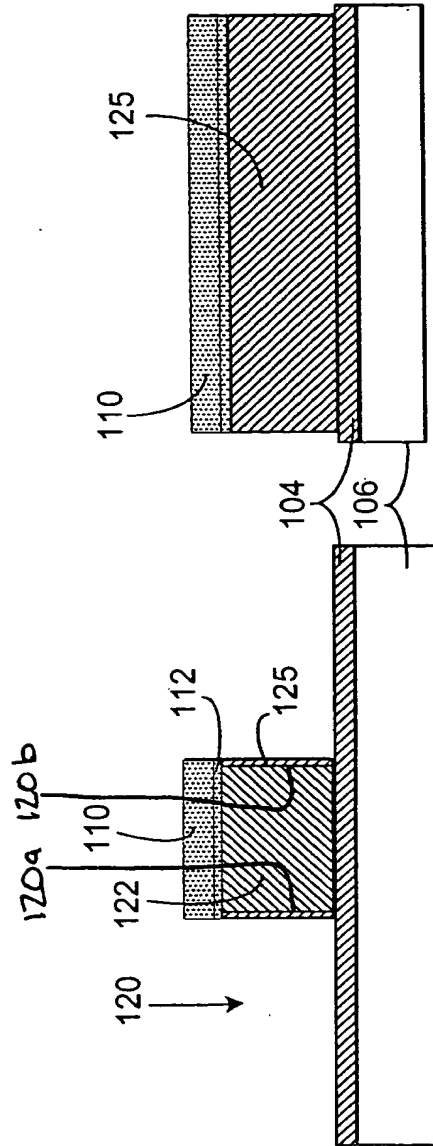


FIG. 4B

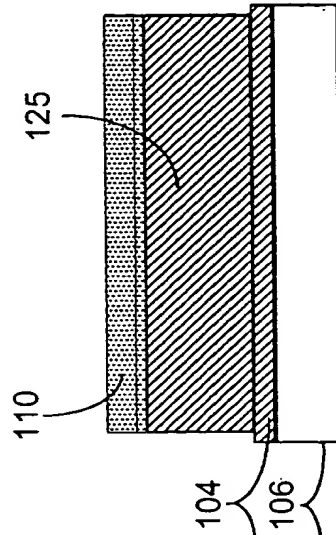


FIG. 4C

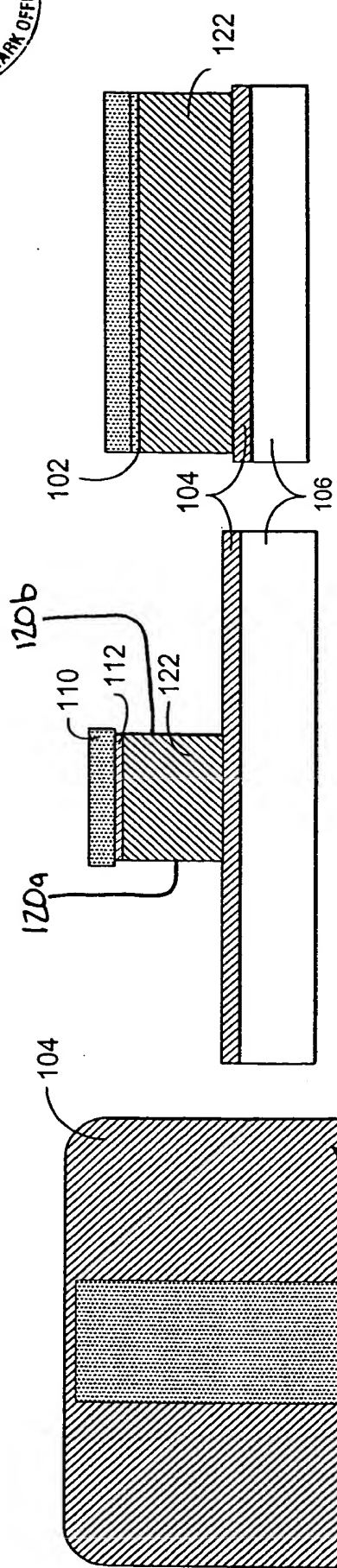


FIG. 5C

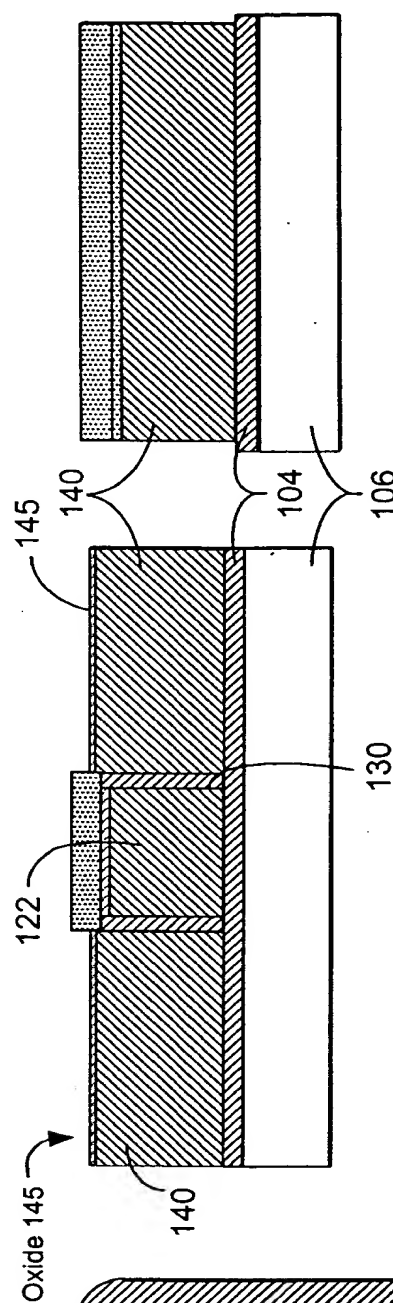


FIG. 6C

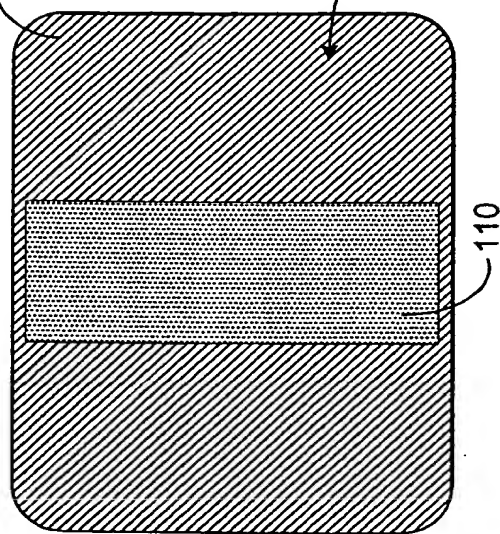


FIG. 5A

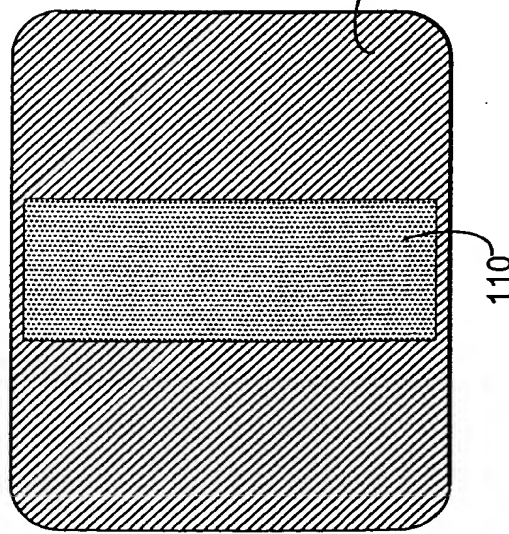
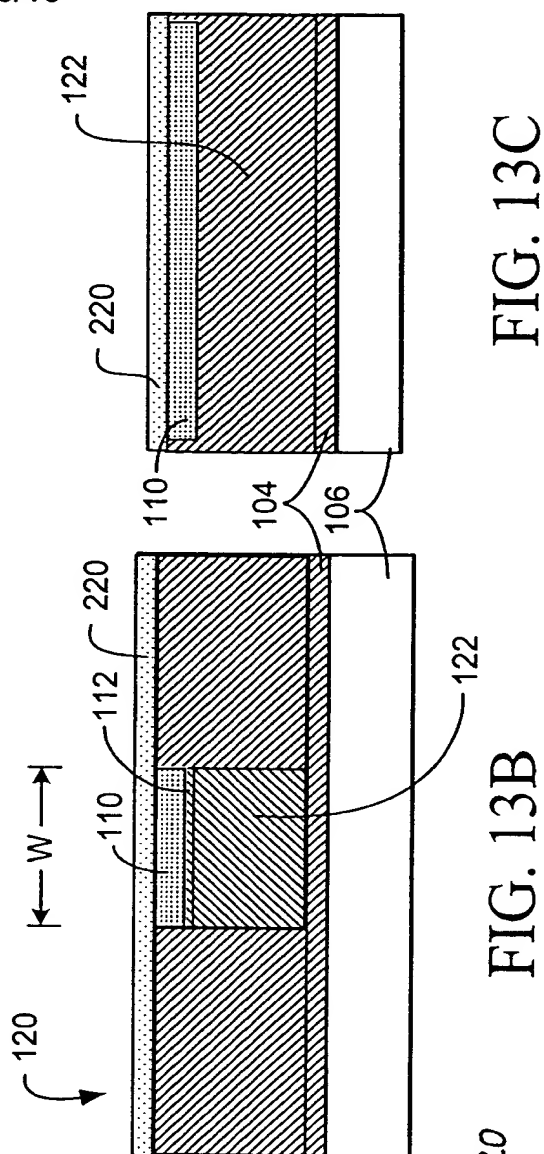
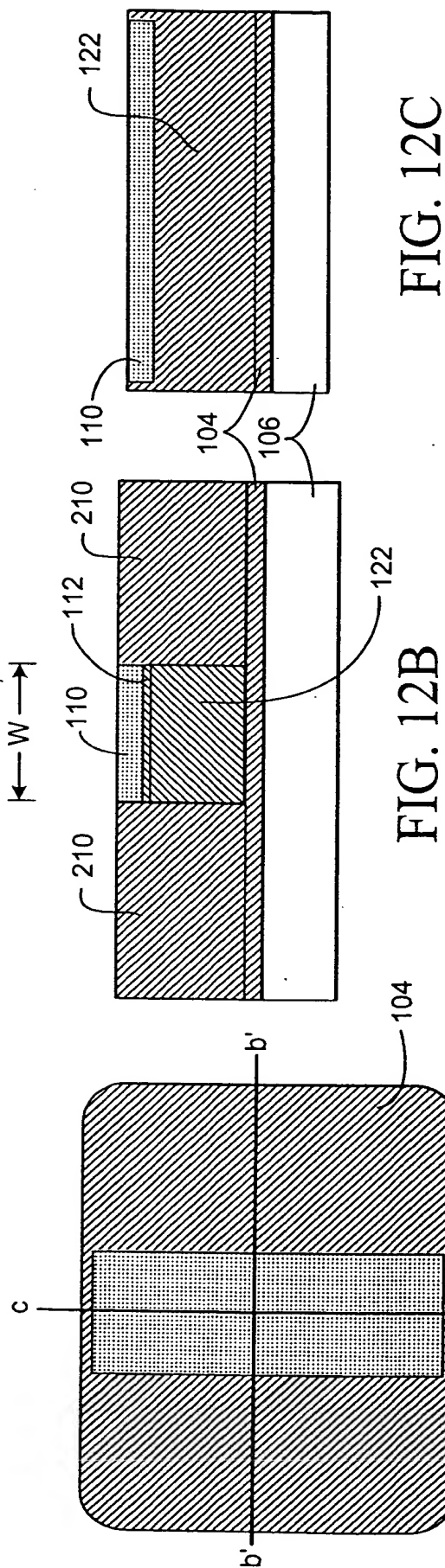


FIG. 6A



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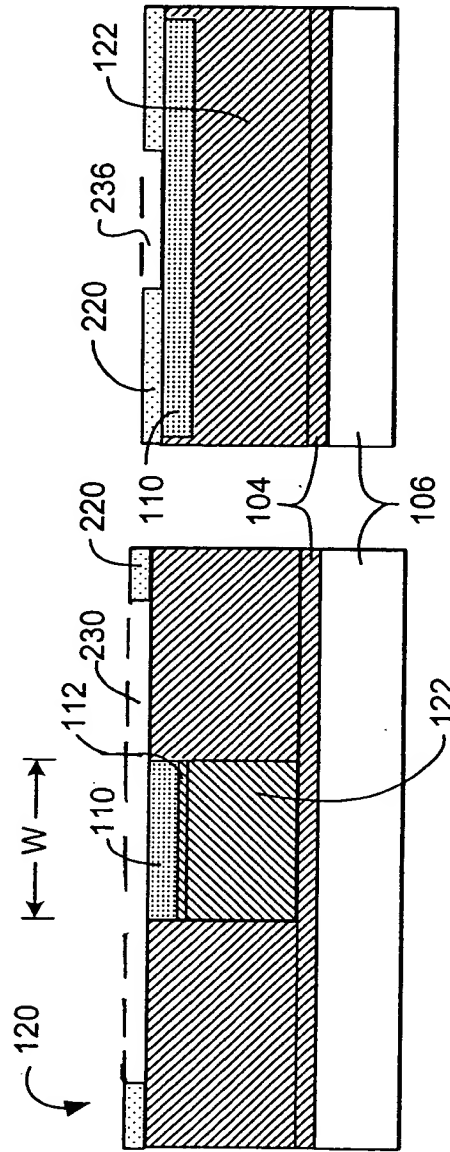


FIG. 14C

FIG. 14B

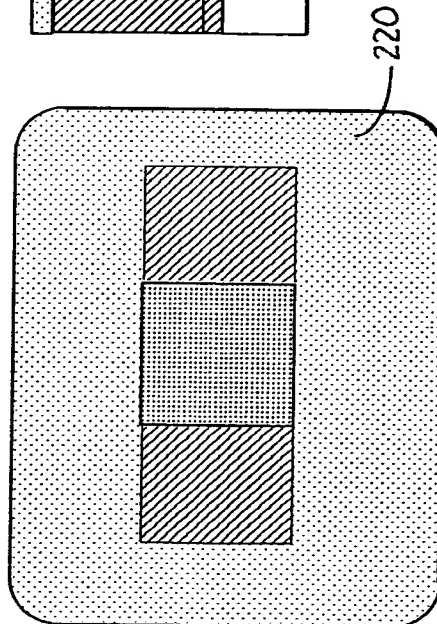


FIG. 14A

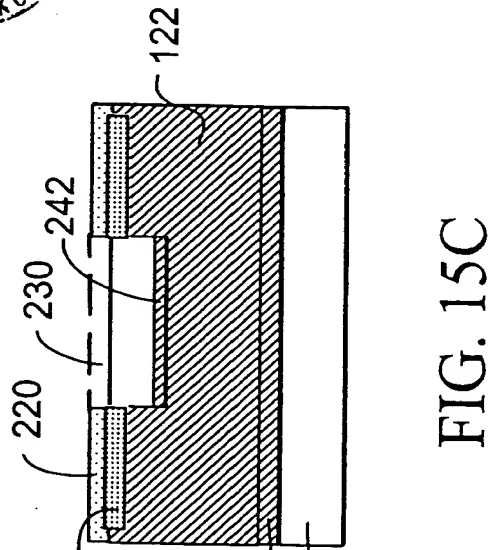
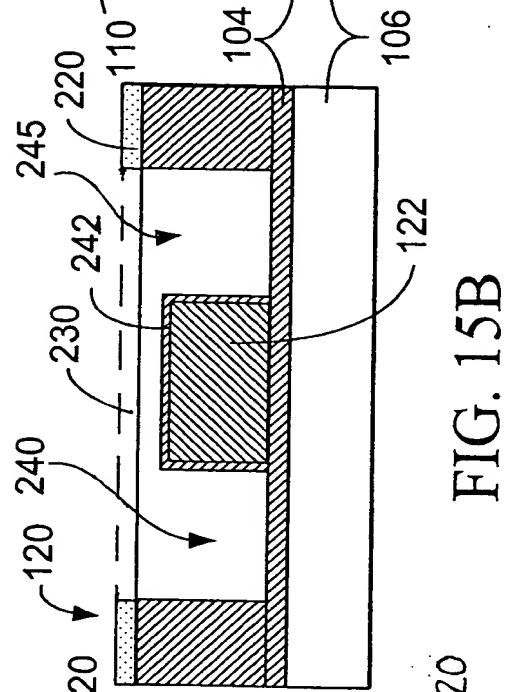
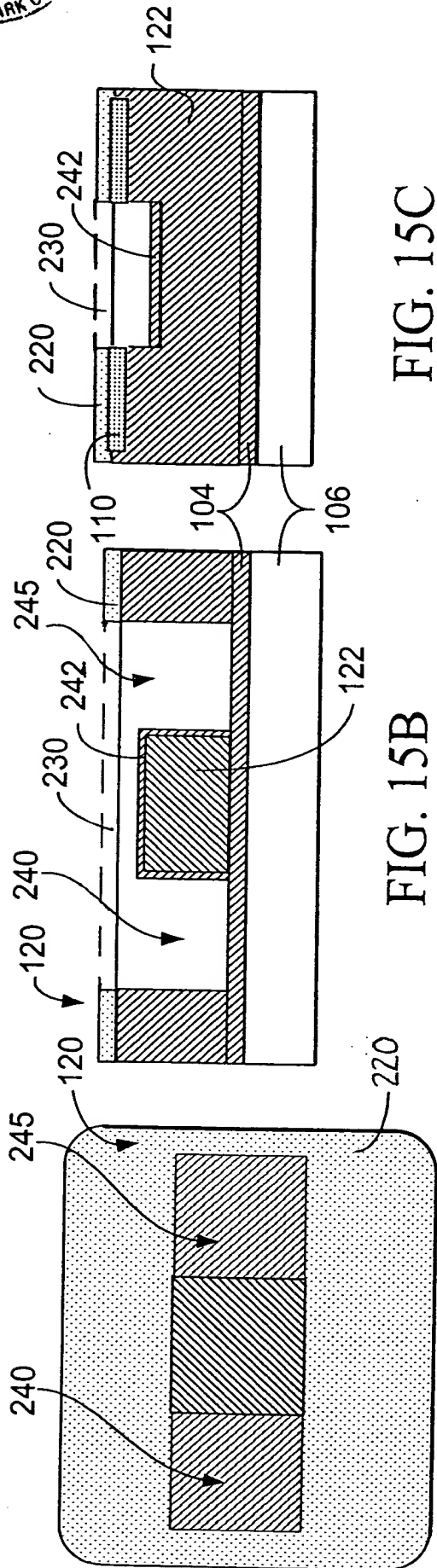


FIG. 15A

FIG. 15B

FIG. 15C

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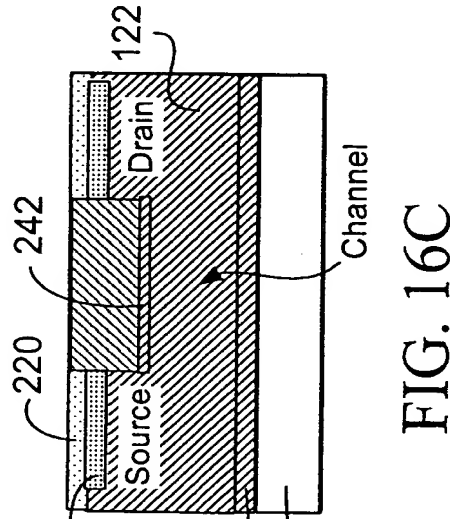
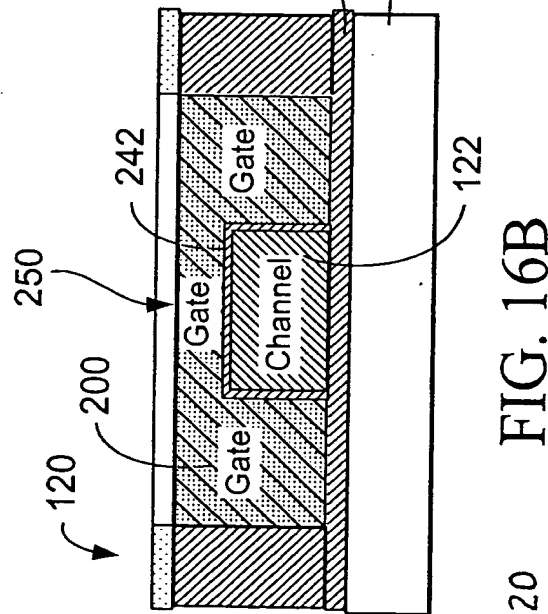
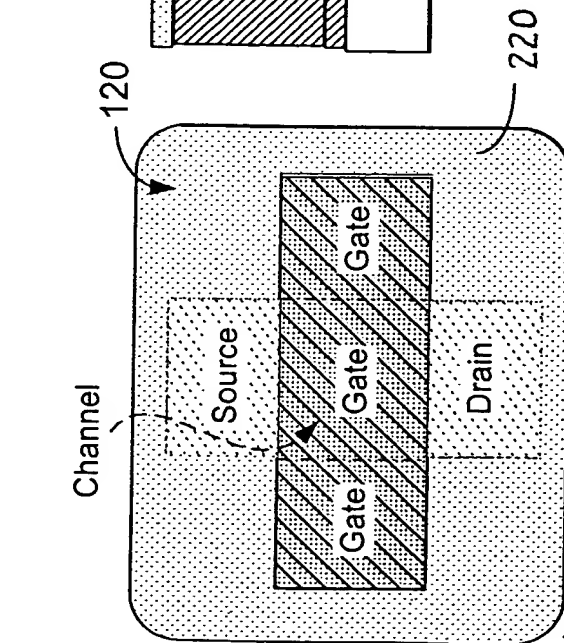


FIG. 16A

FIG. 16B

FIG. 16C

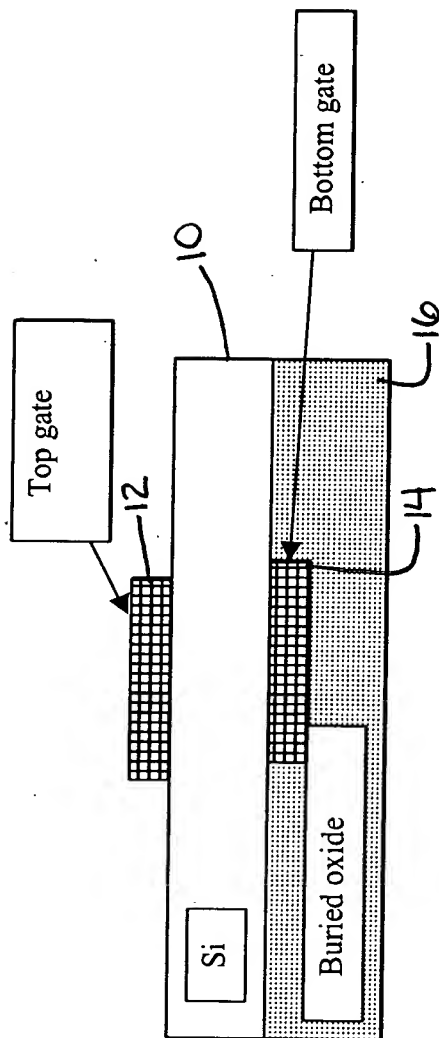


FIG. 1

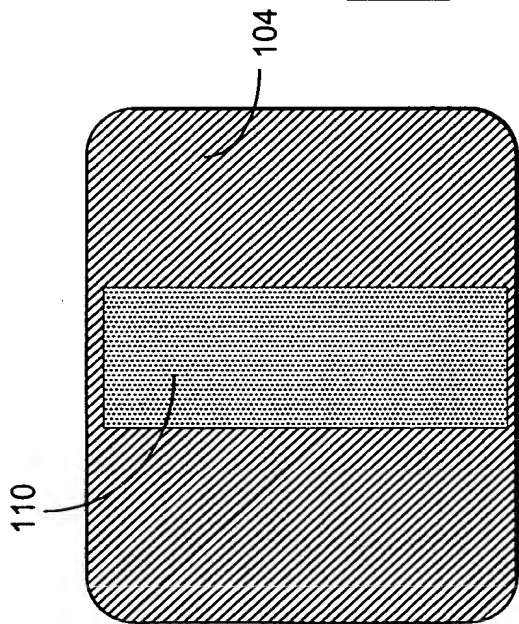


FIG. 4A

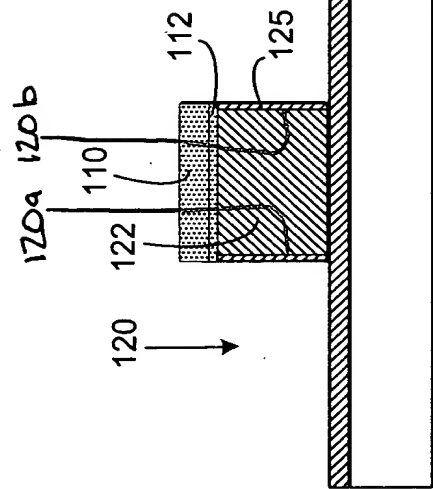


FIG. 4B

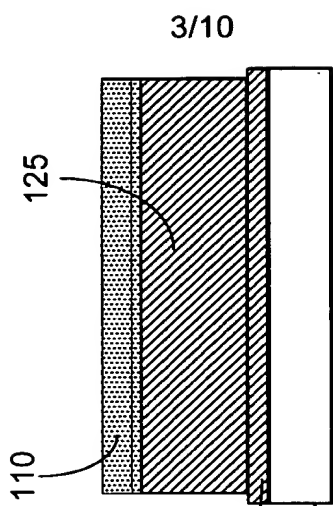


FIG. 4C



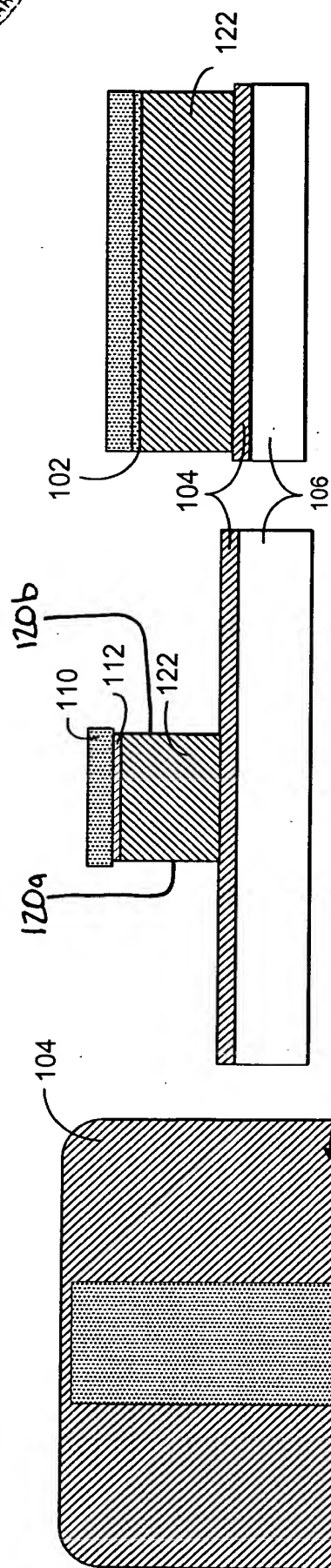


FIG. 5C

FIG. 5A

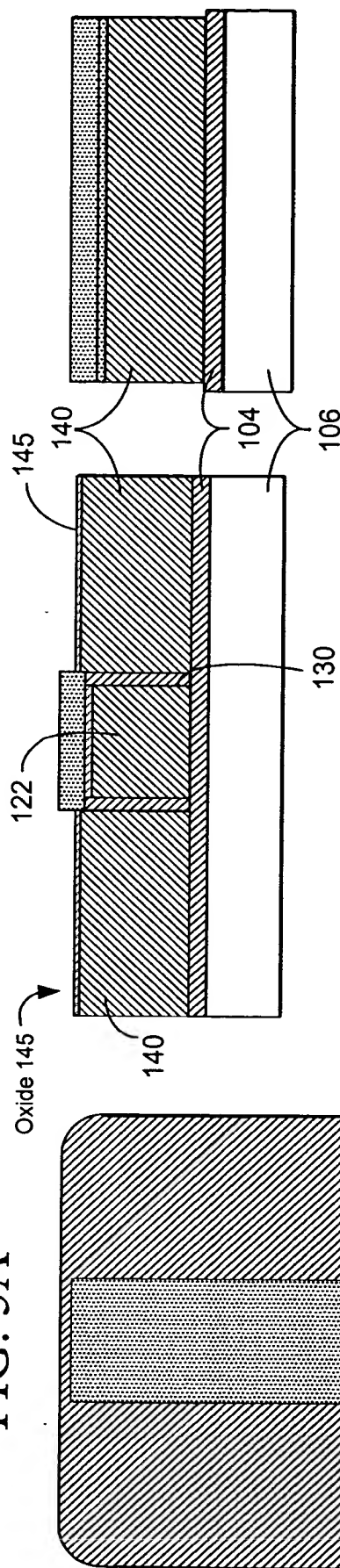


FIG. 6C

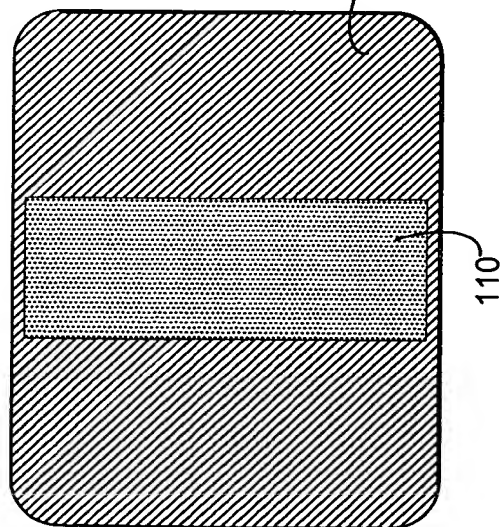


FIG. 6A

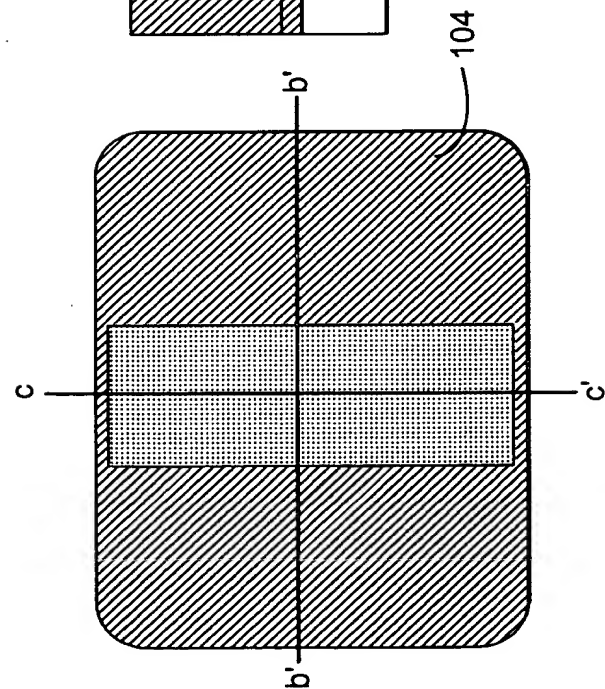


FIG. 12A

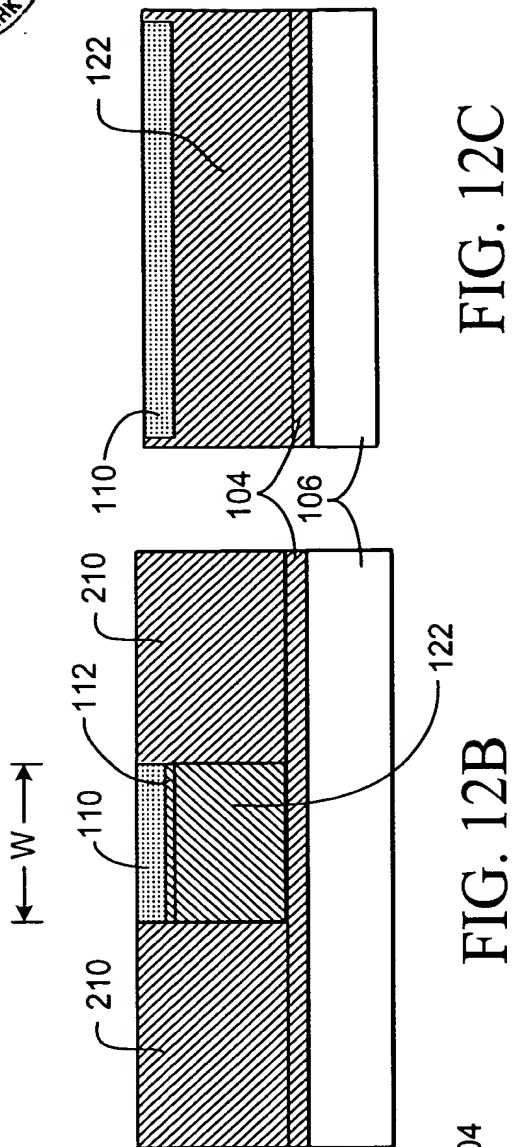


FIG. 12B

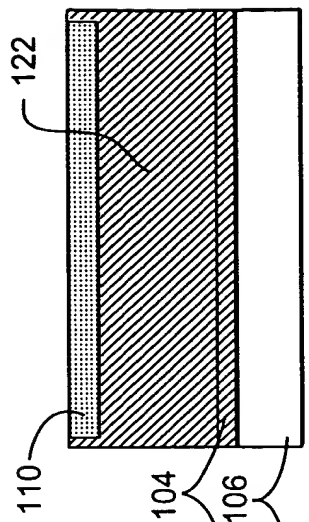


FIG. 12C

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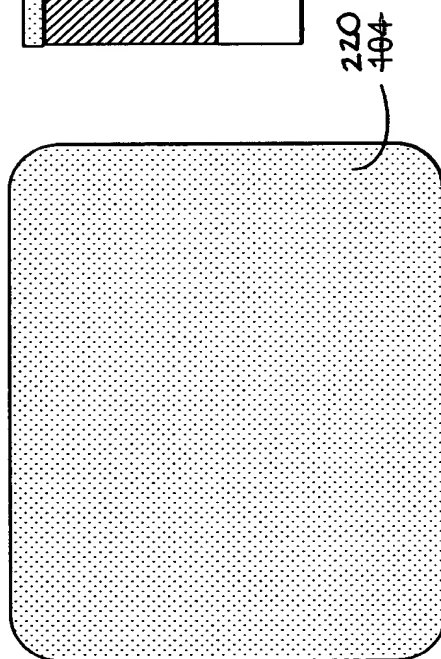


FIG. 13A

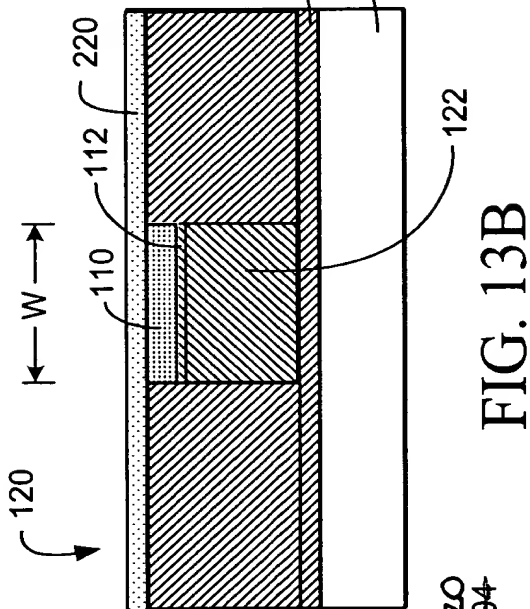


FIG. 13B

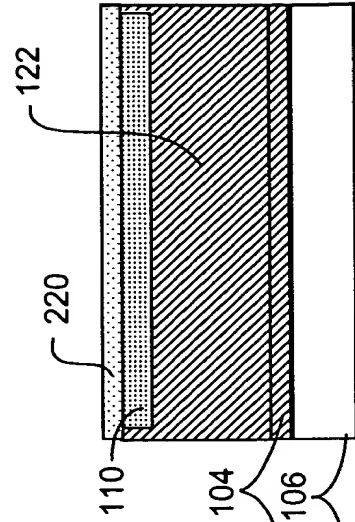


FIG. 13C

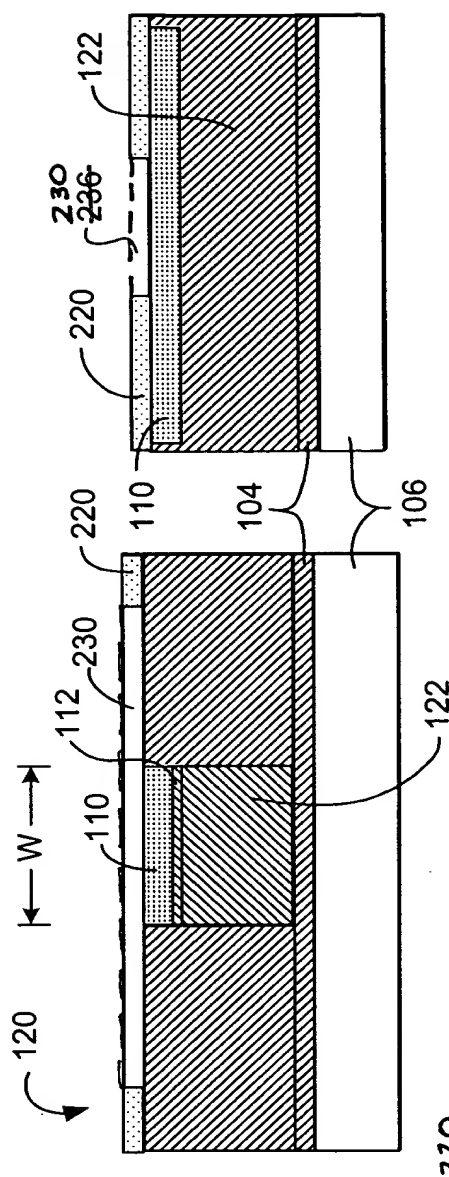


FIG. 14B

FIG. 14C

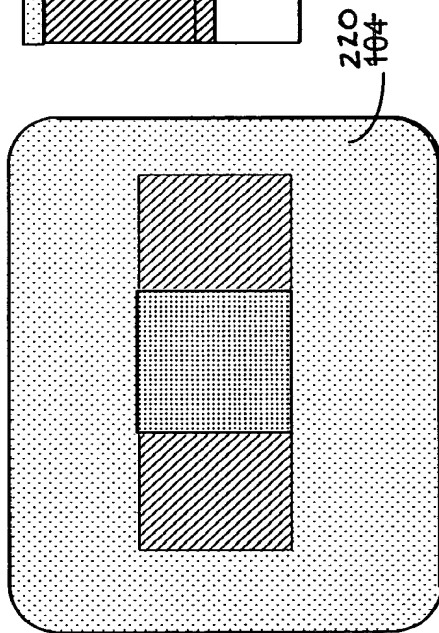


FIG. 14A

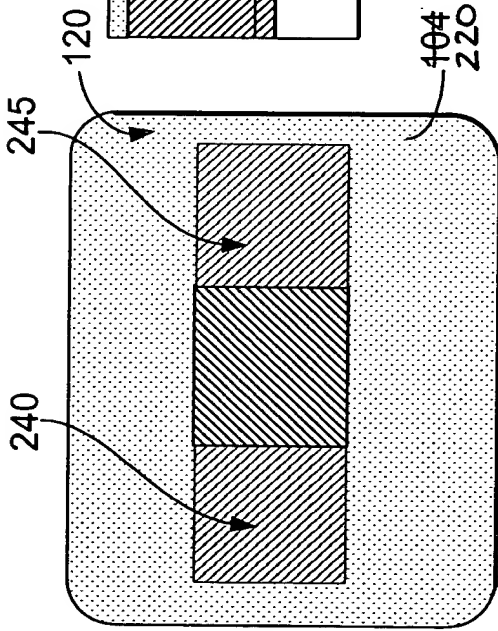


FIG. 15A

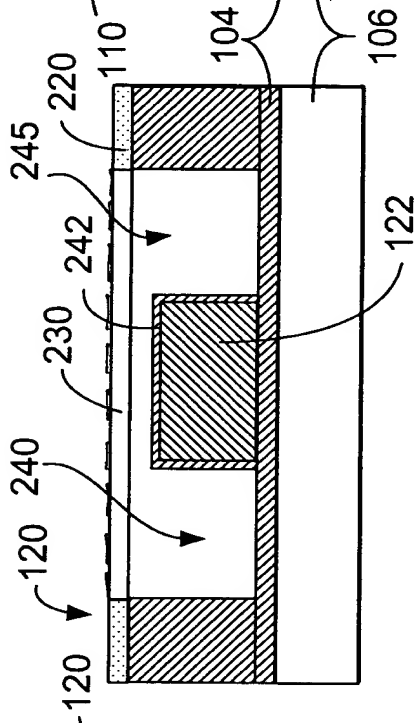


FIG. 15B

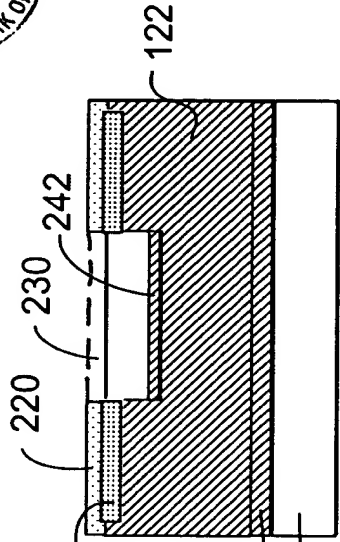


FIG. 15C

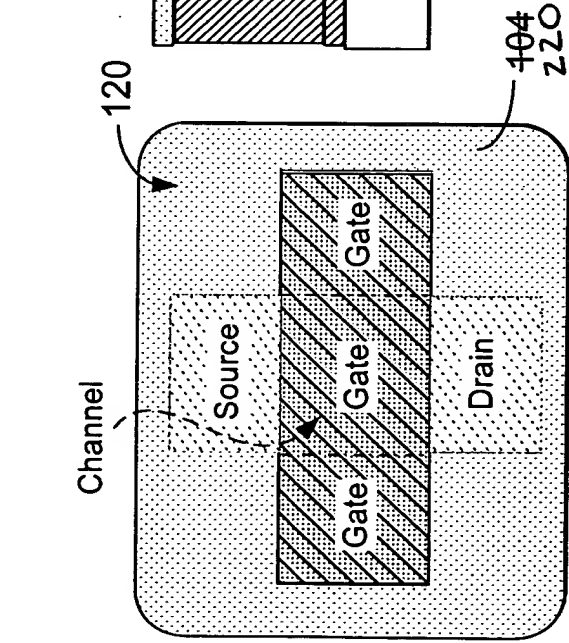


FIG. 16A

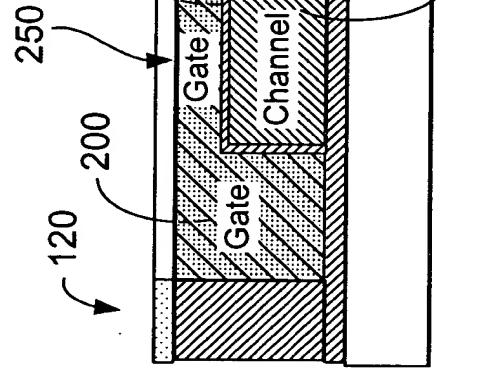


FIG. 16B

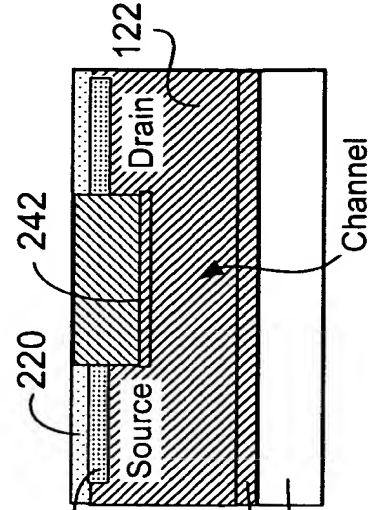


FIG. 16C